

**DECLARATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named inventors are the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **"METHODS OF CONTROLLING PROPERTIES AND CHARACTERISTICS OF A GATE INSULATION LAYER BASED UPON ELECTRICAL TEST DATA, AND SYSTEM FOR PERFORMING SAME"** the Specification of which:

☒ is attached hereto.  
☐ was filed on \_\_\_\_\_ as Application Serial No. \_\_\_\_\_

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent, United States provisional application(s), or inventor's certificate listed below and have also identified below any foreign application for patent, United States provisional application, or inventor's certificate having a filing date before that of the application on which priority is claimed:

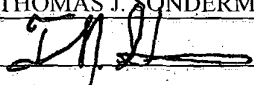
PRIORITY APPLICATION(S)			Priority Claimed
(Number)	(Country)	(Date Filed)	Yes/No
(Number)	(Country)	(Date Filed)	Yes/No

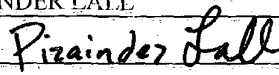
I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56, which become available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)
(Application Serial No.)	(Filing Date)	(Status)

I hereby direct that all correspondence and telephone calls be addressed to J. Mike Amerson, Williams, Morgan & Amerson, P.C., 10333 Richmond, Suite 1100, Houston, Texas 77042, (713) 934-4055.

I HEREBY DECLARE THAT ALL STATEMENTS MADE OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

Inventor's Full Name: THOMAS J. SONDERMAN  
Inventor's Signature:   
Country of Citizenship: USA Date: 6/10/03  
Residence Address: (street, number, city, state, and/or country) 16010 BRAESGATE DRIVE  
AUSTIN, TX 78717  
Post Office Address: Same  
(if different from above)

Inventor's Full Name: PIRAINDER LALL  
Inventor's Signature:   
Country of Citizenship: USA Date: 6/10/03  
Residence Address: (street, number, city, state, and/or country) ~~4064A EILERS~~ 4606A Eilers Ave  
AUSTIN, TX 78751  
Post Office Address: Same  
(if different from above)

PSL  
6-10-03

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

THOMAS J. SONDERMAN  
PIRAINDER LALL

Serial No.: Unknown

Filed: Concurrently Herewith

For: METHODS OF CONTROLLING PROPERTIES  
AND CHARACTERISTICS OF A GATE  
INSULATION LAYER BASED UPON  
ELECTRICAL TEST DATA, AND SYSTEM  
FOR PERFORMING SAME

§ 100.00

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket: 2000.100800/TT5273

**POWER OF ATTORNEY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The undersigned, being the inventors named in the above-identified application; hereby revoke any previous Powers of Attorney and appoint:

Elizabeth A. Apperley, Reg. No. 36,428; Bradley Botsch, Reg. No. 34,552; Michael Caywood, Reg. No. 37,797; Daniel R. Collopy, Reg. No. 33,667; Kent Cooper, Reg. No. 37,296; Paul S. Drake, Reg. No. 33,491; Keith Witek, Reg. No. 37,475; and Harry A. Wolin, Reg. No. 32,638 of Advanced Micro Devices, Inc.; and

Danny L. Williams, Reg. No. 31,892; Terry D. Morgan, Reg. No. 31,181; J. Mike Amerson, Reg. No. 35,426; Kenneth D. Goodman, Reg. No. 30,460; Jeffrey A. Pyle, Reg. No. 34,904; Jaison C. John, Reg. No. 50,737; Ruben S. Bains, Reg. No. 46,532; Scott F. Diring, Reg. No. 35,119; George J. Oehling, Reg. No. 40,471; Shelley P.M. Fussey, Reg. No. 39,458; Mark D. Moore, Reg. No. 42,903; Raymund F. Eich, Reg. No. 42,508; Daren C. Davis, Reg. No. 38,425; Stephanie A. Wardwell, Reg. No. 48,025; and Mark W. Sincell, Reg. No. 52,226; of Williams, Morgan & Amerson, P.C.,

as its attorney or agent so long as they remain with such firms, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Please direct all communications as follows:

J. Mike Amerson  
WILLIAMS, MORGAN & AMERSON, P.C.  
10333 Richmond, Suite 1100  
Houston, Texas 77042  
(713) 934-4055

Signature:

THOMAS J. SONDERMAN

Date:

6/10/23

Signature:

PIRAINDER LALL

Date:

6/10/03